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† Debugger x86/x84 provides access to “Basic Debugging Intel® x86/x64” (training_debugger_x86.pdf). † Intel Processor Trace provide access to “Intel® Processor Trace Training” (training_ipt_trace.pdf). This manual teaches you how to configure the Intel ® Processor Trace, how to record trace information, how to analyze and display the recorded information. † OS Linux x86/x64 provides access to “Training Linux Debugging for Intel® x86/x64” (training_rtos_linux_x86.pdf).

Intel® x86/x64 Debugger - Lauterbach

x64dbg can debug both x64 and x32 applications. There is only one interface. Built on open-source libraries. x64dbg uses Qt, TitanEngine, Zydis, Yara, Scylla, Jansson, lz4, XEDParse, asmjit and snowman. Simple, powerful development. x64dbg uses C++ and Qt to quickly add new features.

x64dbg

The TRACE32 QuadProbe is designed especially for Intel® x86/x64 server platforms. It provides debug capability for up to 4 debug connectors on a single target. Debug licenses are programmed into the QuadProbe. The license to debug the Intel ® x86/x64 architecture DEBUG-INTEL-X86-A is programmed in any case.

Tools for Intel® x86/x64 - Lauterbach

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Intel X86 X64 Debugger - tuttobilardo.it

Online x86 / x64 Assembler and Disassembler This tool takes x86 or x64 assembly instructions and converts them to their binary representation (machine code). It can also go the other way, taking a hexadecimal string of machine code and transforming it into a human-readable representation of the instructions.

Online x86 and x64 Intel Instruction Assembler

Developing under Win7 Pro x64 + VS 2008 Pro. If I enable /debug:parallel in my x64 program, it crashes with: The application was unable to start correctly (0xc000007b). Click OK to close the application If I check my exe with Dependency Walker, I see that it has linked against the x86 PDBX.dll and x86 LIBIOMP5MD.DLL. Obviously this leads to the crash.

x64 code with /debug:parallel links against x86 Parallel ...

Hi, I am a new user of Intel VF - until now, I used Compaq VF 6.6c. I am trying to understand the differences in the two environments, and especially those necessary to create 64-bit executables from my old Fortran sources - which worked fine under CVF in 32-bit. My problem is, When I try to debug t...

64-bit debugger - Intel Community

Pin is a tool for the instrumentation of programs. It supports the Linux*, OS X* and Windows* operating systems and executables for the IA-32, Intel(R) 64 and Intel(R) Many Integrated Core architectures. Pin allows a tool to insert arbitrary code (written in C or C++) in arbitrary places in the executable.

Pin: Pin 3.6 User Guide - Intel Developer Zone

Assembly debugger (x86)

Assembly debugger (x86)

Support for over 80 microprocessor architectures, e.g. ARM, Cortex, Power Architecture, Intel x86/x64, etc. Universal debug module, connect to target via architecture-dependent debug cables: Universal debug module, connect to target via CombiProbe for debugging and 4-bit System Trace: USB3.0 interface to all hosts

Power Debug Interface USB 3 - Lauterbach

The ScanWorks embedded diagnostics solution is able to access the Intel x86 processor through its debug port to diagnose faults and failures in the system. Some of the typical debugger functions provided by ScanWorks embedded diagnostics include reading and writing to all x86 registers, to memory, and to I/O interfaces; setting and retrieving breakpoints; and single-stepping through code.

JTAG-based Embedded Debugger diagnoses Intel x86 systems.

x86-64 (also known as x64, x86_64, AMD64 and Intel 64) is a 64-bit version of the x86 instruction set, first released in 1999.It introduced two new modes of operation, 64-bit mode and compatibility mode, along with a new 4-level paging mode.. With 64-bit mode and the new paging mode, it supports vastly larger amounts of virtual memory and physical memory than was possible on its 32-bit ...

x86-64 - Wikipedia

On the x86 architecture, a debug register is a register used by a processor for program debugging. There are six debug registers, named DR0...DR7, with DR4 and DR5 as obsolete synonyms for DR6 and DR7. The debug registers allow programmers to selectively enable various debug conditions associated with a set of four debug addresses. Two of these registers are used to control debug features. These registers are accessed by variants of the MOV instruction. A debug register may be ...

x86 debug register - Wikipedia

FAQs for Intel® x86/x64 JTAG Debugger Search ... It is also possible to use t32mx86, when debugging 64 bit capable targets, if it is running 32 bit code only. NOTE: The question of t32mx86 or t32mx64 is only related to the target you are debugging.

TRACE32® FAQs for Intel® x86/x64 JTAG Debugger

The x86 version simply maps 200 MB of process memory at 0x30000000 during early program initialization for use as the shadow memory area. Global reporting and x64 debug assertion. The x86->x64 cross compiler can cause issues with global variables currently.

AddressSanitizer for Windows: x64 and Debug Build Support ...

DRProbe ===== This software is a library for accessing the debug registers (watchpoints and breakpoints) on intel x86-64 computers, without the need to use ptrace. This is free software, and is licensed under the GPL, version 3. A version of this license was included with this software in a file named LICENSE.

GitHub - blucia0a/DRProbe: Library for accessing debug ...

bddisasm is a fast, lightweight, x86/x64 instruction decoder. The project also features a fast, basic, x86/x64 instruction emulator, designed specifically to detect shellcode-like behavior.

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